



Features

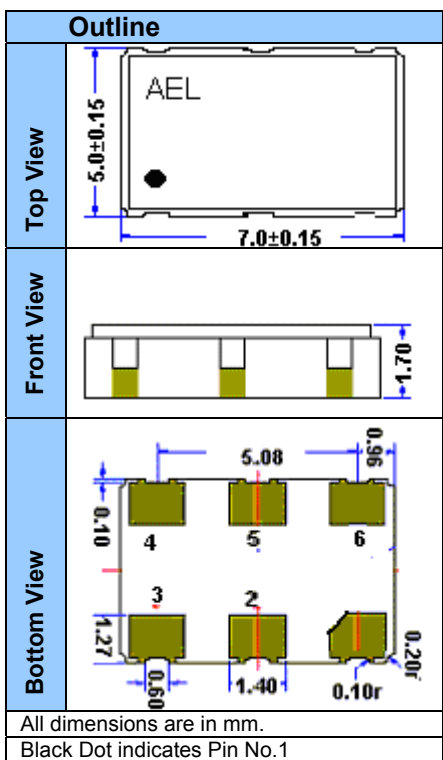
- Frequency 1 – 250MHz
- Package 7X5 mm Ceramic
- LVDS Output
- Low Jitter
- RoHS Compliant Standard

Table 1

Code	Stability Codes		F	G	M
	Frequency Stability		±20 PPM	±25 PPM	±50 PPM
E	Temperature Range	-10°C to +70°C			
I		-40°C to +85°C			

Denotes Available Denotes not Available

Mechanical Specification



Pin Configuration

Pin	Description
1.	Output Enable/Disable
2.	No Connect
3.	Ground
4.	Output
5.	Output*
6.	Supply

Parameters	Conditions		
	1.8V	2.5V	3.3V
Series:	XO 560		
Frequency:	1- 250MHz		
Supply Voltages:	1.8V ± 5%	2.5V ± 10%	3.3V ± 10%
Differential Output:	200 –450mV	250 – 450mV	250-450mV
Output Offset Voltage: ≥ 80MHz < 80MHz	0.81 0-1.38 V	1.125-1.375 V 1.125-1.500 V	1.125-1.375 V 1.125-1.500V
Supply Current: ≥ 80MHz < 80MHz	35mA(max)	63mA(max) 40mA(max)	66mA(max) 45mA(max)
Output High Level:		1.60 Volts (max)	1.60 Volts (max)
Output Low Level:		0.90 Volts (min)	0.90 Volts (min)
Differential Output Error(dVos)	50 mV (max)		
Output Symmetry	45% - 55% (Referenced to 50% amplitude)		
Jitter	0.6 pS RMS (max) 12KHz - 20MHz from Fnominal 2.8 pS RMS (max) 10 Hz – 1 MHz from Fnominal		
Rise / Fall time:	300 - 700 pS (≥ 80MHz) 400 - 900 pS (< 80MHz)	Vth is20% and 80% of waveform	
Enable/Disable Internal Pull-Up:	50Kohm (min) To Vcc (Equivalent Resistance)		
Output Waveform:	LVDS		
Frequency stability:	(Table 1) Specify		
Temperature range:	(Table 1) Specify		
V Disable: (Referenced to Ground)	0.18V (max)	0.4V (max)	0.8Volts (max)
V Enable: (Referenced to Ground)	1.54V (min)	2.0Volts (min)	
Output Leakage:	±10 uA (Vout = Vcc) pad1 low, device disabled ±10 uA (Vout = 0V) pad1 low, device disabled		
Enable Time:	10nS (max) Time for output to reach a logic state		
Disable Time:	10nS (max) Time for output to reach a high Zstate		
Start up time:	Measured From Time, Vcc = 2.2V 5mS (max) ≥ 80 MHz 3mS (max) < 80 MHz	Measured From Time, Vcc =2.2V	Measured From Time, Vcc = 3.0V
Operating Temperature Range:	-10°C - +70°C (Standard Temperature Range) -45°C - +85°C (Extended Temperature Range)		
Storage temperature range:	-55°C to +125° C		
Stand By Current: Pad1Low, DeviceDisable	2mA to 3mA	3 uA (max) ≥ 80MHz 1.5mA (max) < 80 MHz	

Specifications subject to change without notice
Revision No. 12b of May 2008

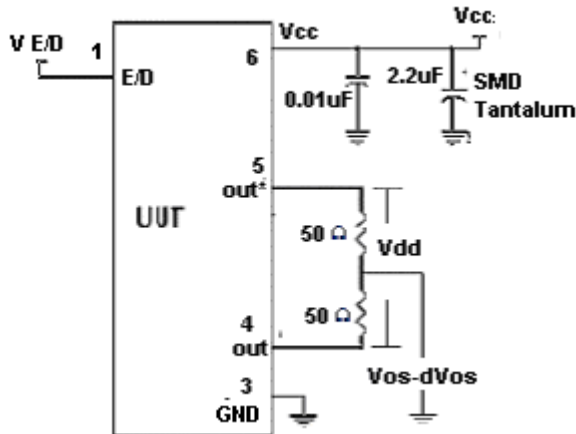
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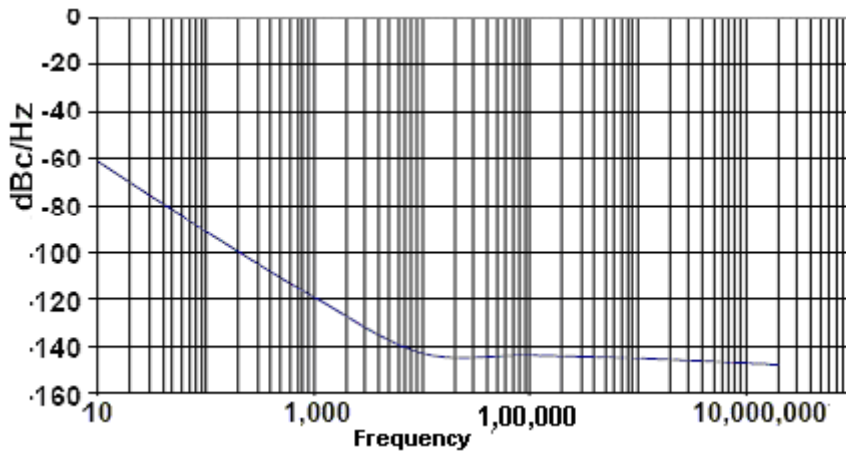
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LOAD CIRCUIT

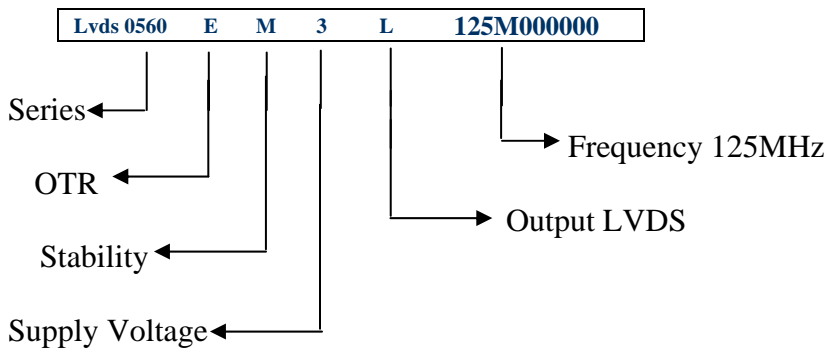


Typical Phase-Noise Response



Ordering Information

Example



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