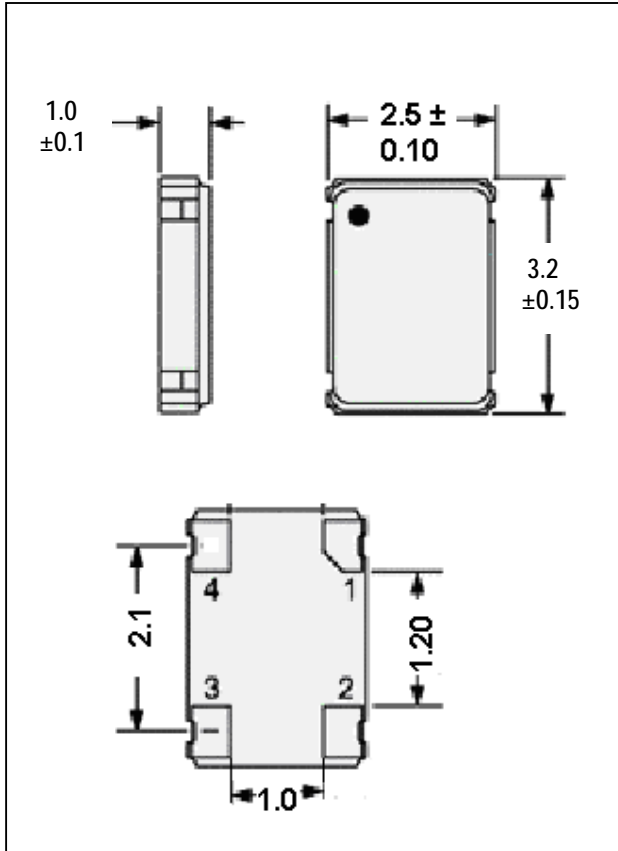




**FEATURE**

- Low Height
- Wide frequency range from 1.5 MHz to 120 MHz
- High speed CMOS
- RoHS Compliant Standard

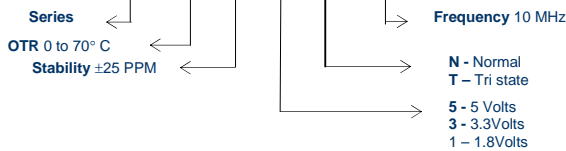


Pin Configuration  
 Pin 1: No Connection  
           E/D Control (Optional)  
 Pin 2: Ground  
 Pin 3: Output  
 Pin 4: DC Input

**Ordering Information**

**Example**

CXO 3225 F G 5 N 010M000000



Parameters	
Frequency Range:	1.5 MHz –120 MHz
Output Drive:	1 to 10 TTL gates (or) 15 pF to 50 pF
Logic Levels:	For 5V, 3.3V, 1.8V - '0' = 0.1V <sub>dd</sub> Max. '1' = +0.9V <sub>dd</sub> min
Rise / Fall time:	10 ns max.
Start up time:	10 ms max.
Waveform Symmetry:	60/40% (or) 40/ 60 % (Typical)
Output Waveform:	Square Wave
Frequency Stability:	Select from table below
Temperature Range:	Select from table below
Supply Voltage (V <sub>cc</sub> ):	+5.0V DC (±10%) +3.3V DC (±10%) +2.5V DC (±10%) +1.8V DC (±10%)
Supply Current:	For 5.0 Volts: 45 mA max For 3.3 Volts: 30 mA max For 3.3 Volts: 30 mA max For 1.8 Volts: 20 mA max
Ageing:	± 5 ppm per year max.
Storage Temperature Range:	-40 to +85 °C

Operating Temperature Range	
Code	Range
F	0°C to +70°C
E	-10°C to +70°C
C	-20°C to +70°C
I	-40°C to +85°C

Frequency Stability (Inclusive of Operating Temperature Range)	
Code	Stability
G	± 25 PPM
M	± 50 PPM
K	± 100 PPM

Specifications subject to change without notice  
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